

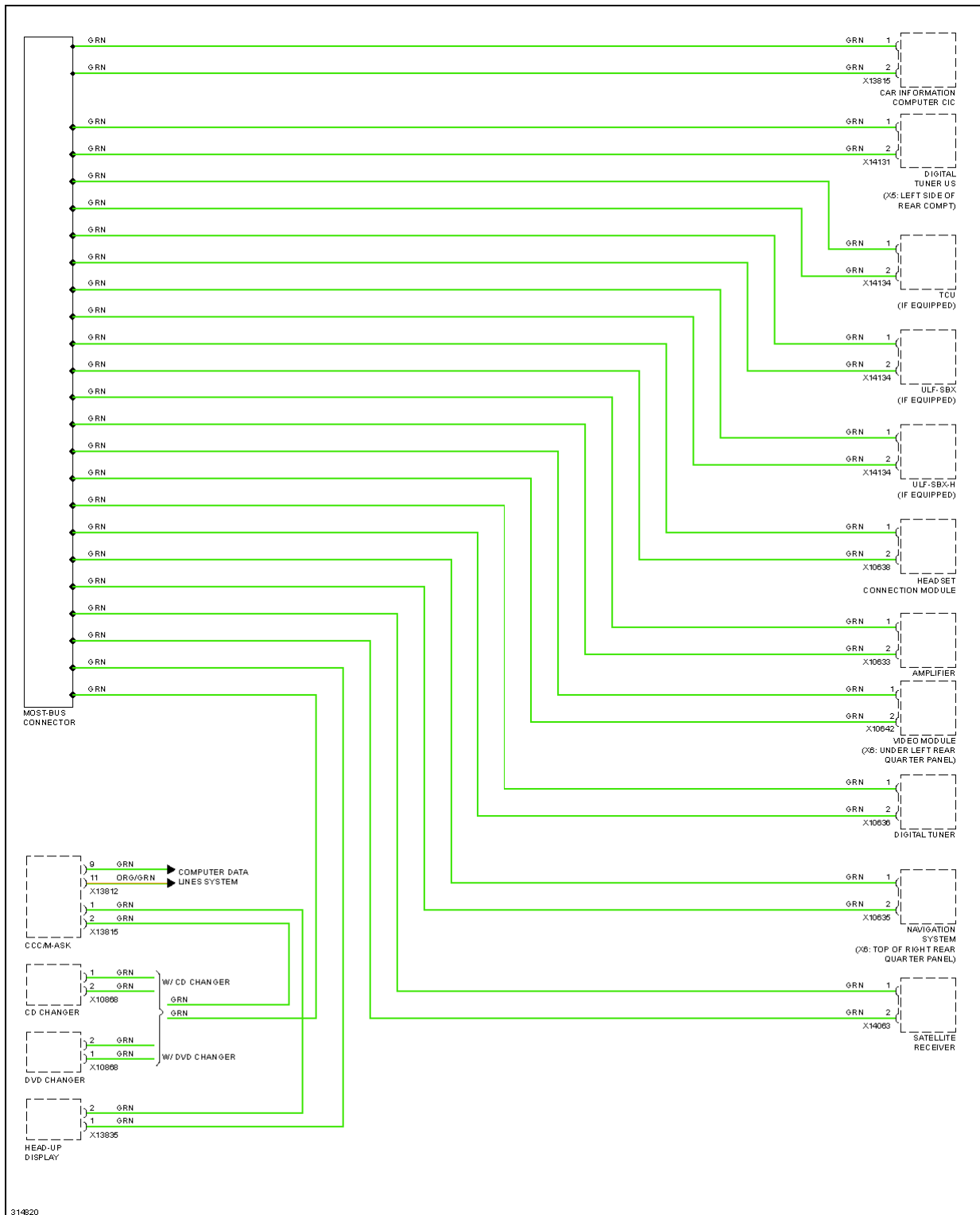


Fig. 103: MOST Data Bus Circuit