

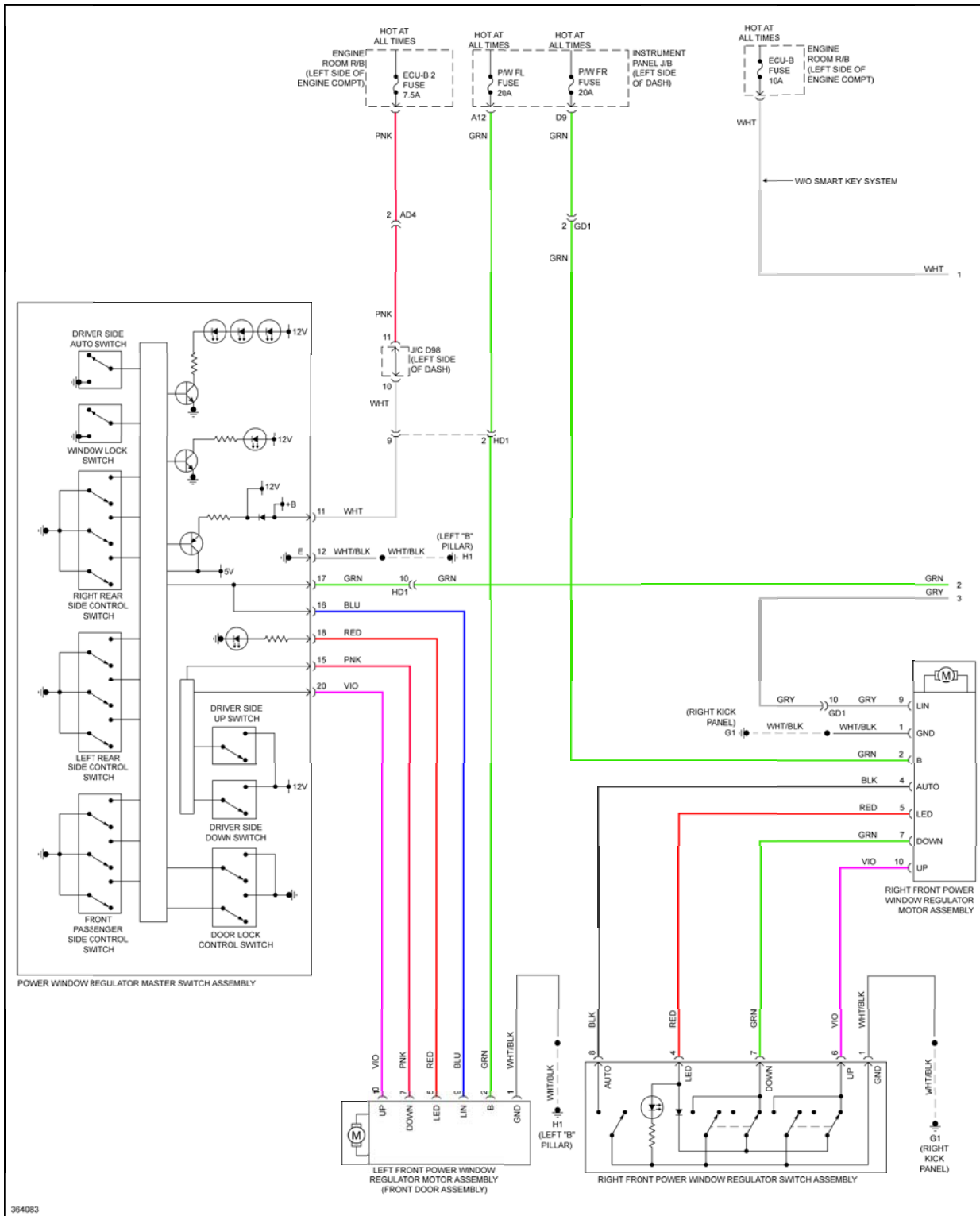


Fig. 90: Power Windows Circuit (1 of 2)

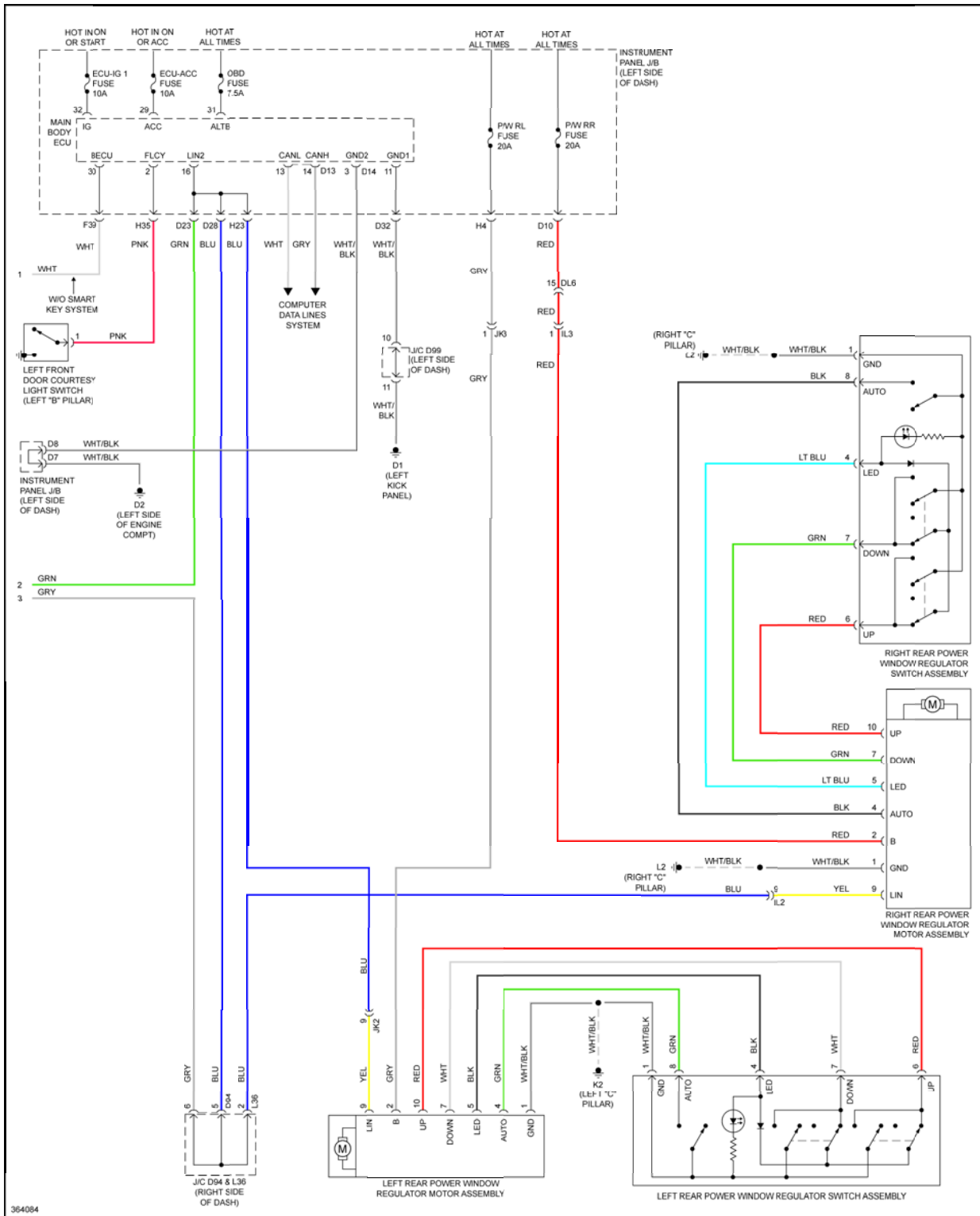


Fig. 91: Power Windows Circuit (2 of 2)