

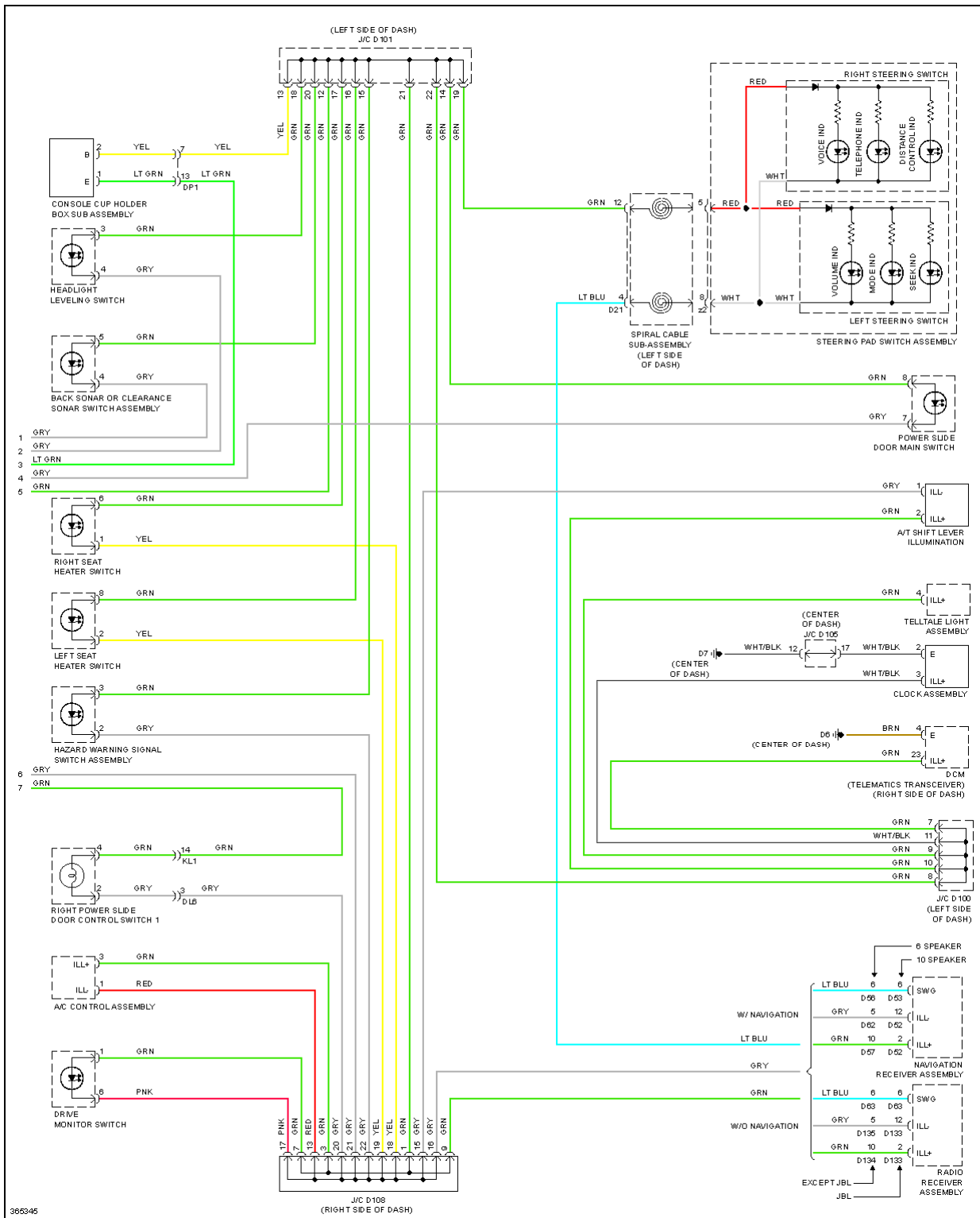


Fig. 56: Instrument Illumination Circuit (2 of 2)

MEMORY SYSTEMS