

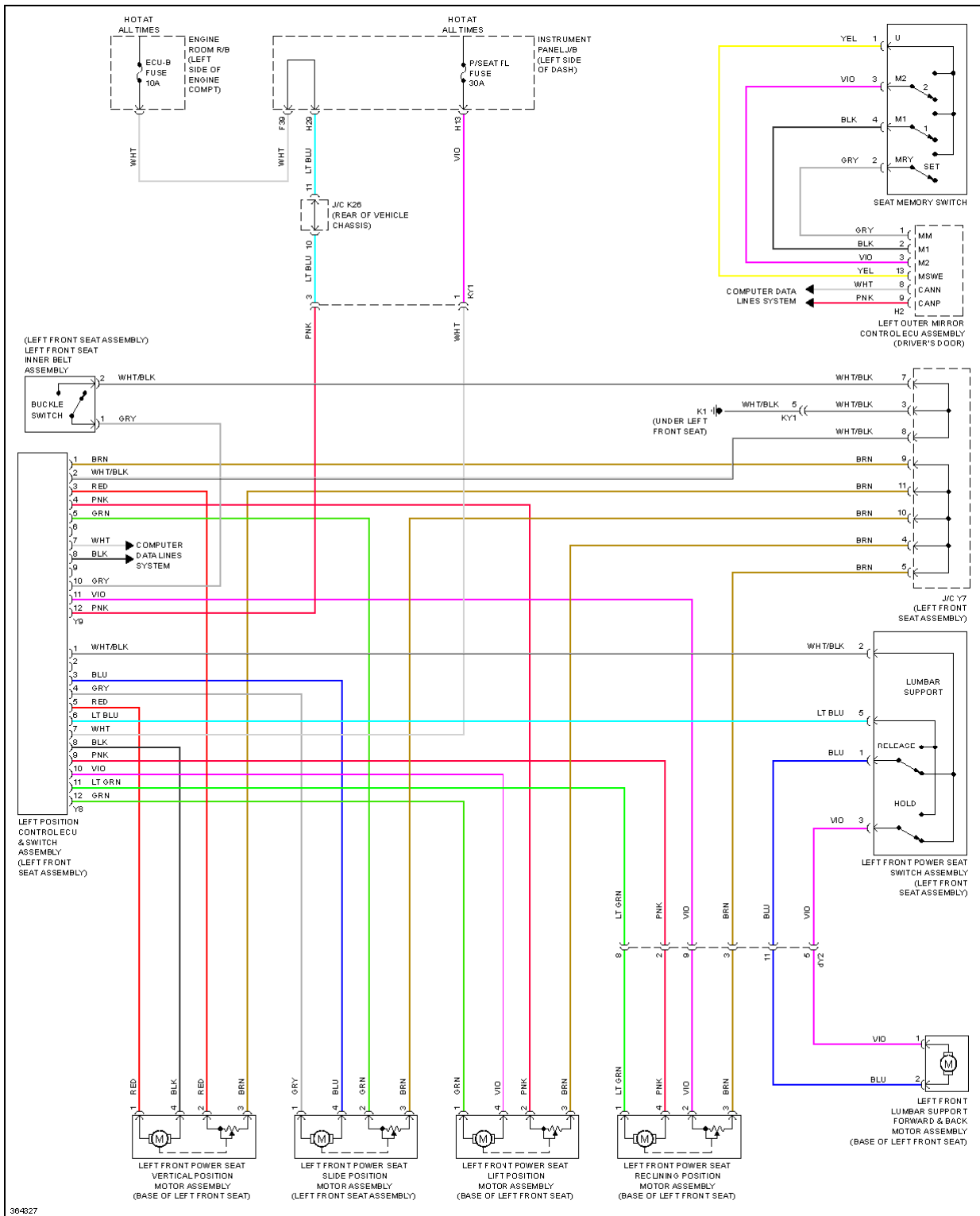


Fig. 59: Memory Seat Circuit

NAVIGATION