

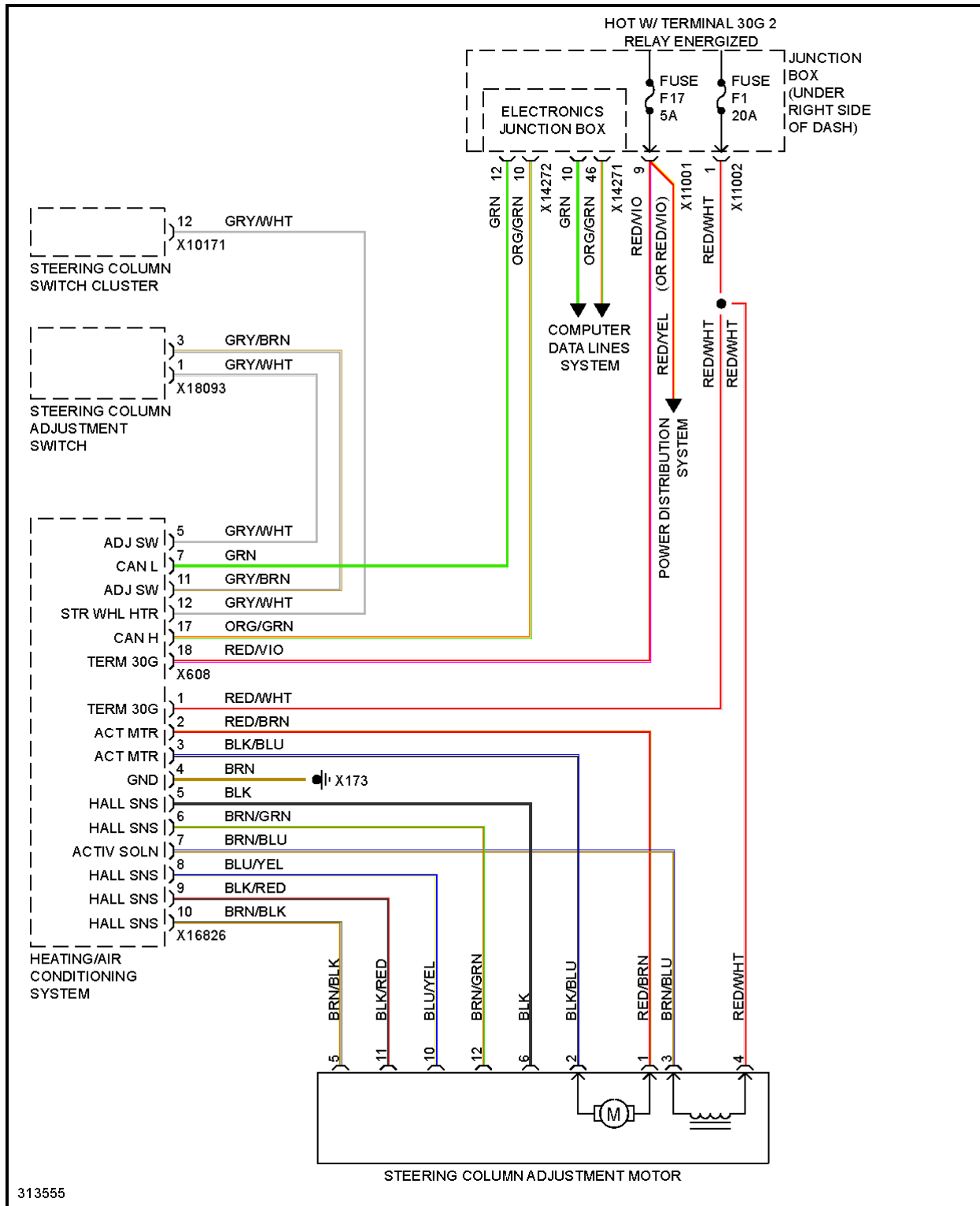


Fig. 66: Steering Column Memory Circuit

