

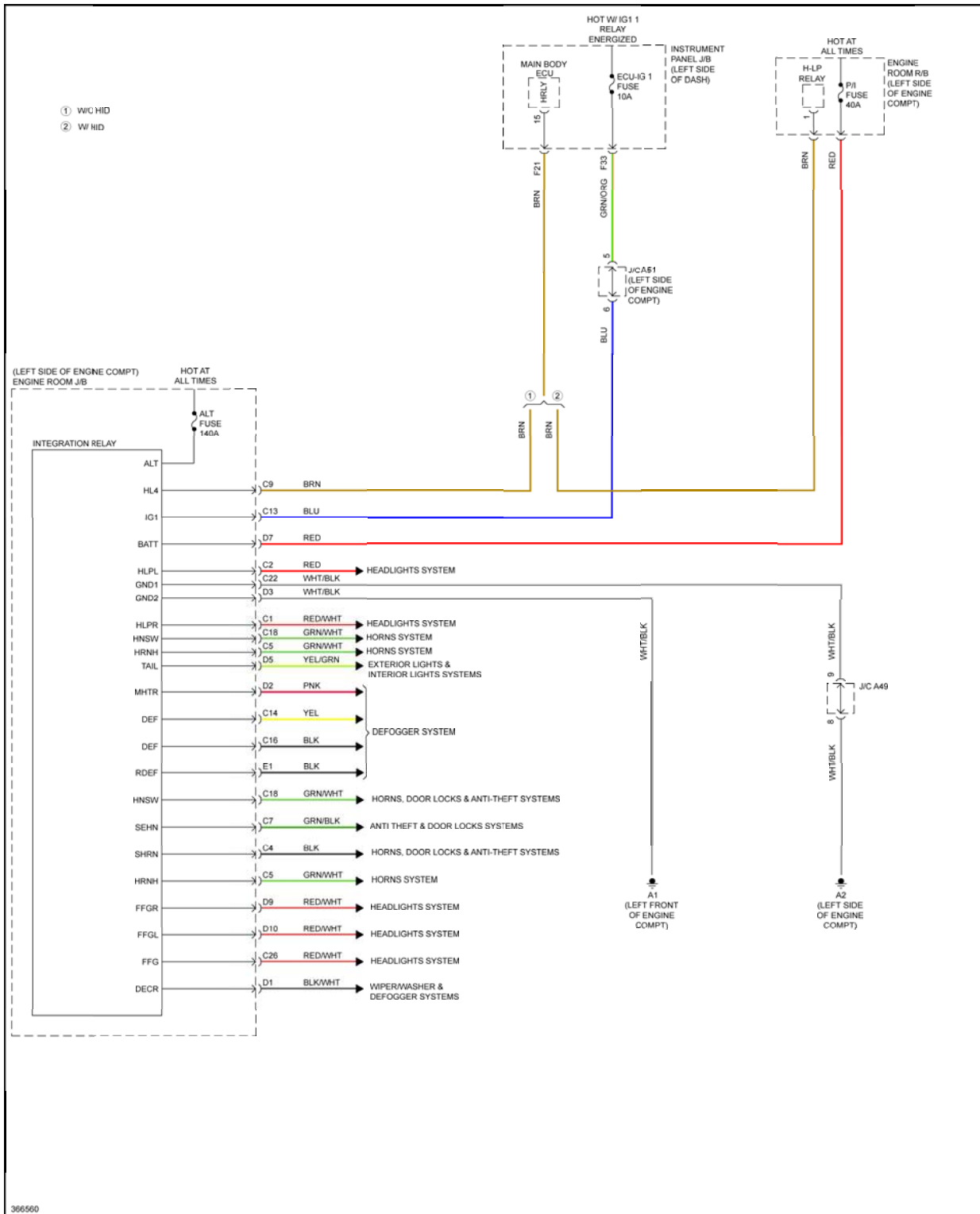


Fig. 15: Integration Control and Panel Circuit

COMPUTER DATA LINES