

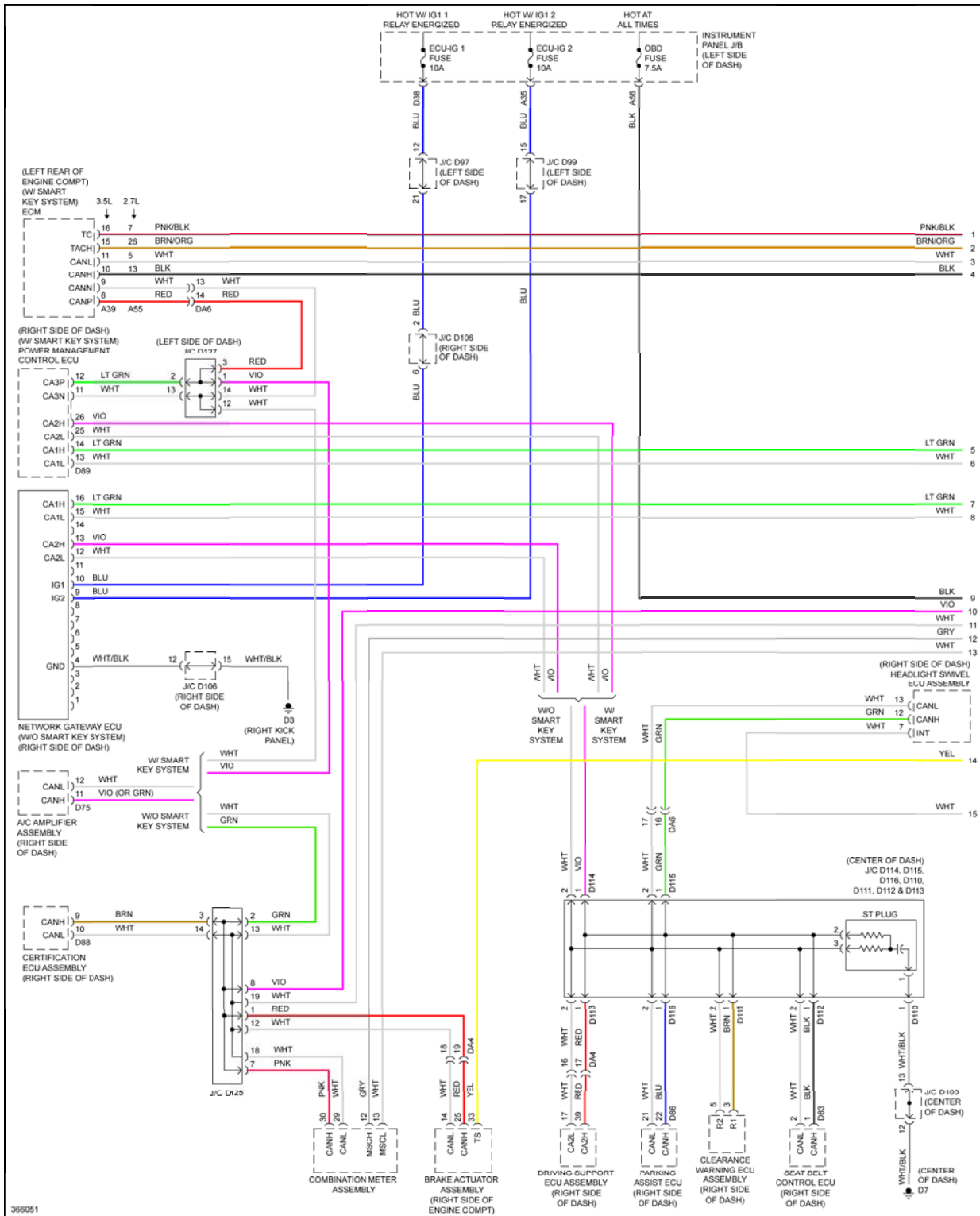


Fig. 16: Computer Data Lines Circuit (1 of 2)

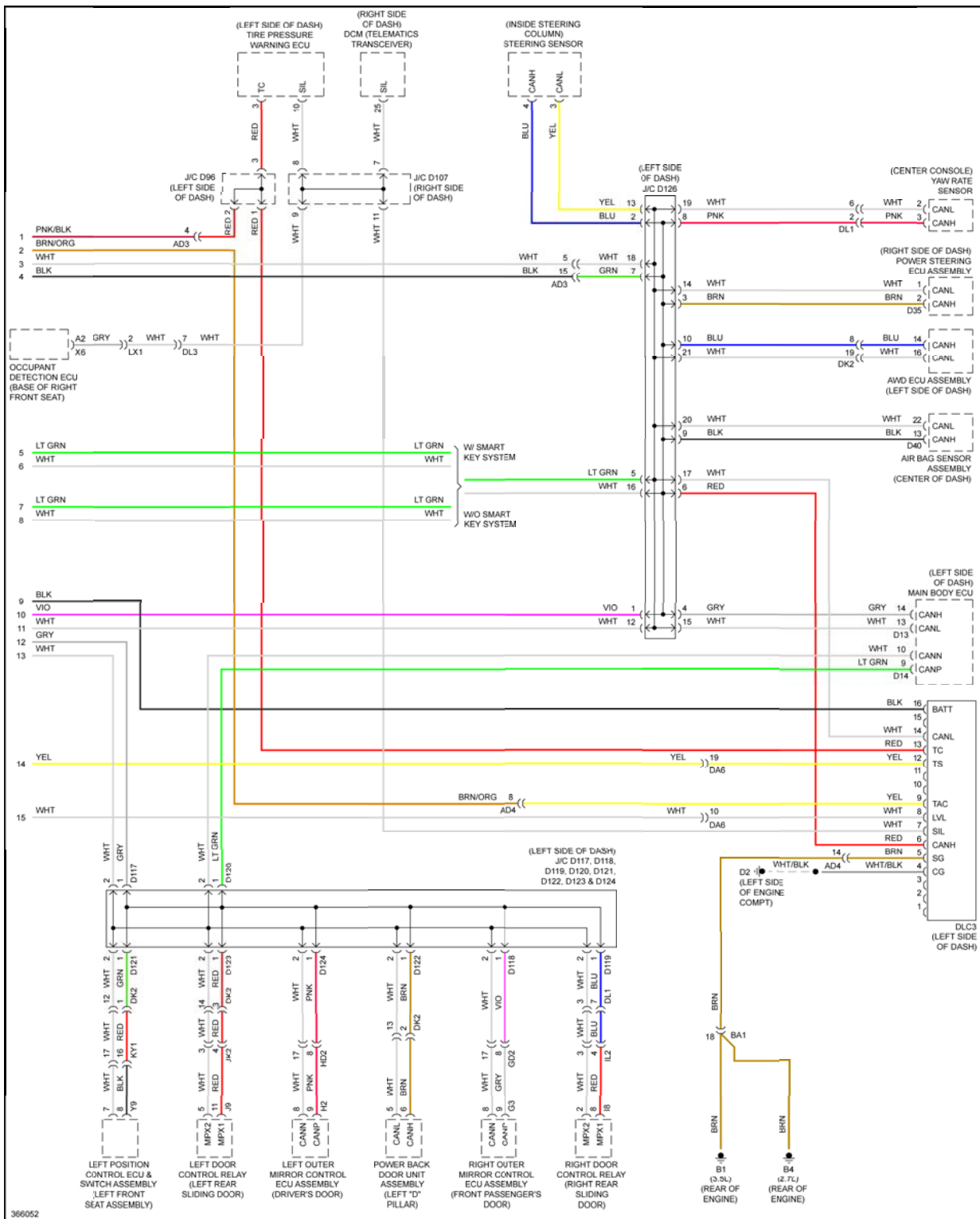


Fig. 17: Computer Data Lines Circuit (2 of 2)

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